

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	1879	chip adj interface	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:31
2	BRS	L2	39373	macro	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:31
3	BRS	L3	133266 4	substrate	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:31
4	BRS	L4	41321	ASIC	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:31
5	BRS	L5	8	1 same 2	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:37
6	BRS	L6	4	1 and 2 and 4 and 3	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:38
7	BRS	L7	60	data adj flow adj chip	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:39

	Type	L #	Hits	Search Text	DBs	Time Stamp
8	BRS	L8	136	chip adj macro	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:39
9	BRS	L9	1	7 and 8	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:39
10	BRS	L10	5	schedule adj chip	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:40
11	BRS	L11	50	1 same 4	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:40
12	BRS	L12	3	chip adj chip adj macro	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:41
13	BRS	L13	82	chip adj chip adj2 interface	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:42
14	BRS	L14	34	2 and 13	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB_	2004/07/20 15:44

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	Type	L #	Hits	Search Text	DBs	Time Stamp
15	BRS	L15	0	20030110339.URPN.	USPAT	2004/07/20 15:43
16	BRS	L16	1960	370/229,235,236.ccls.	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB	2004/07/20 15:44
17	BRS	L17	24	2 and 16	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB	2004/07/20 15:46
18	BRS	L18	4187	network adj processor	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB	2004/07/20 15:46
19	BRS	L19	31	2 same 18	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB	2004/07/20 15:51
20	BRS	L20	468	2 same 4	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB	2004/07/20 15:51
21	BRS	L21	10	1 and 20	USPAT ; US-PG PUB; EPO; DERW ENT; IBM_T DB	2004/07/20 15:51

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